Notice of References Cited Application/Control No. 10/814,962 Examiner Evan Pert Applicant(s)/Patent Under Reexamination SANDA ET AL. Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6819117 B2	11-2004	Wilsher	324/601
	В	US-6608494 B1	08-2003	Bruce et al.	324/752
	С	US-6596980 B2	07-2003	Rusu et al.	250/214.1
	D	US-			
	Е	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			,
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	Z					
	0					
	Р					
	Q					
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		/ Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	D	Weger et al., "Transmission Line Pulse Picosecond Imaging Circuit Analysis Methodology for Evaluation of ESD and Latchup", March 30, 2004, 41st Annual 2003 IEEE International Reliability Physics Symposium Proceedings, pages 99-104.
	¸V	
	w	
	Х	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.